# 3D BiCS FLASH™: The Future of High-Density Flash Memory

KIOXIA delivers flash-based products for next-generation storage applications. Having invented NAND flash over 30 years ago, KIOXIA is now one of the world's largest flash memory suppliers – and continues to move the technology forward.

#### BiCS FLASH: Accelerating Beyond 2D

Jan. 2020 Introduced 5<sup>th</sup> generation 112-layer BiCS FLASH increasing cell density by over 20%'



Jul. 2018 Achieved industry's highest capacity of 1.33 terabits for a single chip (QLC BiCS FLASH)



Jun. 2017 First to introduce 4-bit-per-cell technology (QLC BiCS FLASH 3D flash memory)



Aug. 2015 First to introduce 256 gigabit (32GB) 48-layer memory chip (TLC 3-bit-per- cell BiCS FLASH)



Jun.

First to announce 3D flash



The global 3D flash memory market garnered \$9.06 billion in 2017 and is expected to reach \$99.77 billion by 2025.

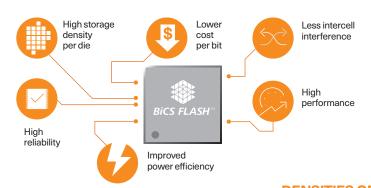
Source: Allied Market Research<sup>4</sup>

#### What is BiCS FLASH™?

KIOXIA's BiCS FLASH is a three-dimensional (3D) vertical flash memory cell structure. This structure enables it to surpass the capacity of mainstream 2D (planar) flash memory.

KIOXIA's TLC 3-bit-per-cell 512Gb (64GB¹) BiCS FLASH, an industry first², enhances the reliability of write/erase endurance while boosting write speeds. The company also offers a 1.33Tb BiCS FLASH device that features 4-bit-per-cell, quadruple-level-cell (QLC), technology. It's the first 3D flash memory device to do so³.





## Why 3D Memory?

Moving to a 3D structure was a necessary evolution from 2D floating gate flash, because the memory cell size for 2D flash had shrunk to its limit. Floating gate's last generation was 15nm, which enabled a density of 128Gb per die. Compared to floating gate, BiCS FLASH 3D flash memory offers higher density, higher endurance, higher performance and better power efficiency. Single die densities of 256Gb and higher are now possible with BiCS FLASH.

### DENSITIES OFFERED

Based on 16-die stacked architecture in a single package

#### TLC Triple-Level Cell

#### 1TB Single-package device with 512Gb die

# **512GB**Single-package device with 256Gb die

#### QLC Quad-Level Cell

## **2.66TB**Single-package device with

## 1.33Tb die BENEFITS OF QLC



Highest density flash (2.66TB single package)



Lowest cost per bit

#### **APPLICATIONS**











**Tablets** 



Smartphones



Gaming

Automotive PCs Data Centers Enterprise



The future of high-density flash memory lies in 3D. Already enabling the enterprise, data center, PC and mobile applications of today, our BiCS FLASH has paved the way for the applications that will turn possibilities into realities. Storage density needs are continually climbing higher and higher – and we designed BiCS FLASH with this in mind."

 $-\textbf{Scott Nelson}, Senior\ \textit{Vice President and General Manager}, \ \textit{Memory Business Unit}, \ \textit{KIOXIA}$ 

[1] Product density is identified based on the density of memory chip(s) within the Product, not the amount of memory capacity evailable for data storage by the end user. Consumer-usable capacity will be less due to overhead data areas, formatting, bad blocks, and other constraints, and may also vary based on the host device and application. For details, please refer to applicable product specifications. The definition of 1Gb = 2°30 bits = 1,073,741,824 bits. The definition of 1Gb = 2°30 bits = 1,073,741,824 bits. The definition of 1Gb = 2°30 bits = 1,073,741,824 bits.

[3] As of June, 2017. KIDXIA survey. [4] Survey. [4] Survey. [4] Survey. [4] Survey. [4] Market Research report: "3D NAND Flash Memory Market by Type (Single-Level Cell, Multi-Level Cell, and Triple-Level Cell), Application (Cameras, Laptop & PCs, Smartphone & Tablets, and Others), and End Users (Automotive, Consumer Electronics, Enterprise, Healthcare, and Others). Global Opportunity Analysis and Industry Forecast, 2017 – 2025." [6] 2020 KIVIXIA Memorica. Inc. [6] 2020 KIV